

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

**Title of  
Invention**

SYSTEM AND METHOD FOR DIRECT WRITE TO DYNAMIC  
RANDOM ACCESS MEMORY (DRAM) USING PFET BIT-SWITCH

Application Number :

Confirmation Number:

First Named Applicant: Darren Anand

Attorney Docket Number: BUR920020145US1

Art Unit:

Examiner:

Search string: ( 6400629 ).pn

### US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
(2)	1	6400629	2002-06-04	Barth, Jr. et al.	—	365	210

### Signature

Examiner Name	Date
<i>Sal Adhlong</i>	<i>4/04</i>